



XA-9630

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Tsuyoshi FUJIWARA et al.

Appln. No.: 10/083,416

Group Art Unit: 2813

Filed: February 27, 2002

Examiner: T. Nguyen

For: METHOD FOR MANUFACTURING SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE COMPRISING A MEMORY CELL AND A
CAPACITOR

Allowed: February 27, 2004

Confirmation No.: 7388

* * *

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The Reasons for Allowance track recitations of independent Claim 1, but the recitations of independent Claims 6, 11, and 14 differ somewhat. Nevertheless, the allowability of independent Claim 6, 11, and 14 is evident from the arguments presented in the Amendment dated November 20, 2003.

The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 any fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this paper and to credit any overpayment to that Account. If any extension

of time is required in connection with the filing of this paper and has not been requested separately, such extension is hereby requested.

Respectfully submitted,

NHS:jab

By: Nelson H. Shapiro
Nelson H. Shapiro
Reg. No. 17,095

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April 20, 2004